Electronic Filing System (EFS) Data **Electronic Patent Application Submission USPTO** Use Only

EFS ID:

58673

Application ID:

10709041

Title of Invention:

LOW TRIGGER VOLTAGE ESD NMOSFET TRIPLE-WELL CMOS

DEVICES

First Named Inventor:

Kiran Chatty

Domestic/Foreign Application: Domestic Application

Filing Date:

2004-04-08

Effective Receipt Date:

2004-04-08

Submission Type:

Utility Patent Filing

Filing Type:

new-utility

Confirmation number:

3040

Attorney Docket Number:

BUR920030188US1

Total Fees Authorized:

810.0

Payment Category:

Deposit Account

Deposit Account Number:

90456

Deposit Account Name:

William D. Sabo

RAM Payment Status:

RAM has not been processed

Digital Certificate Holder: cn=William D. Sabo,ou=Registered Attorneys,ou=Patent and Trademark

Office,ou=Department of Commerce,o=U.S. Government,c=US

Certificate Message Digest: 48cb8a8d63ea895c27285d81bae3a4f144ed3a99